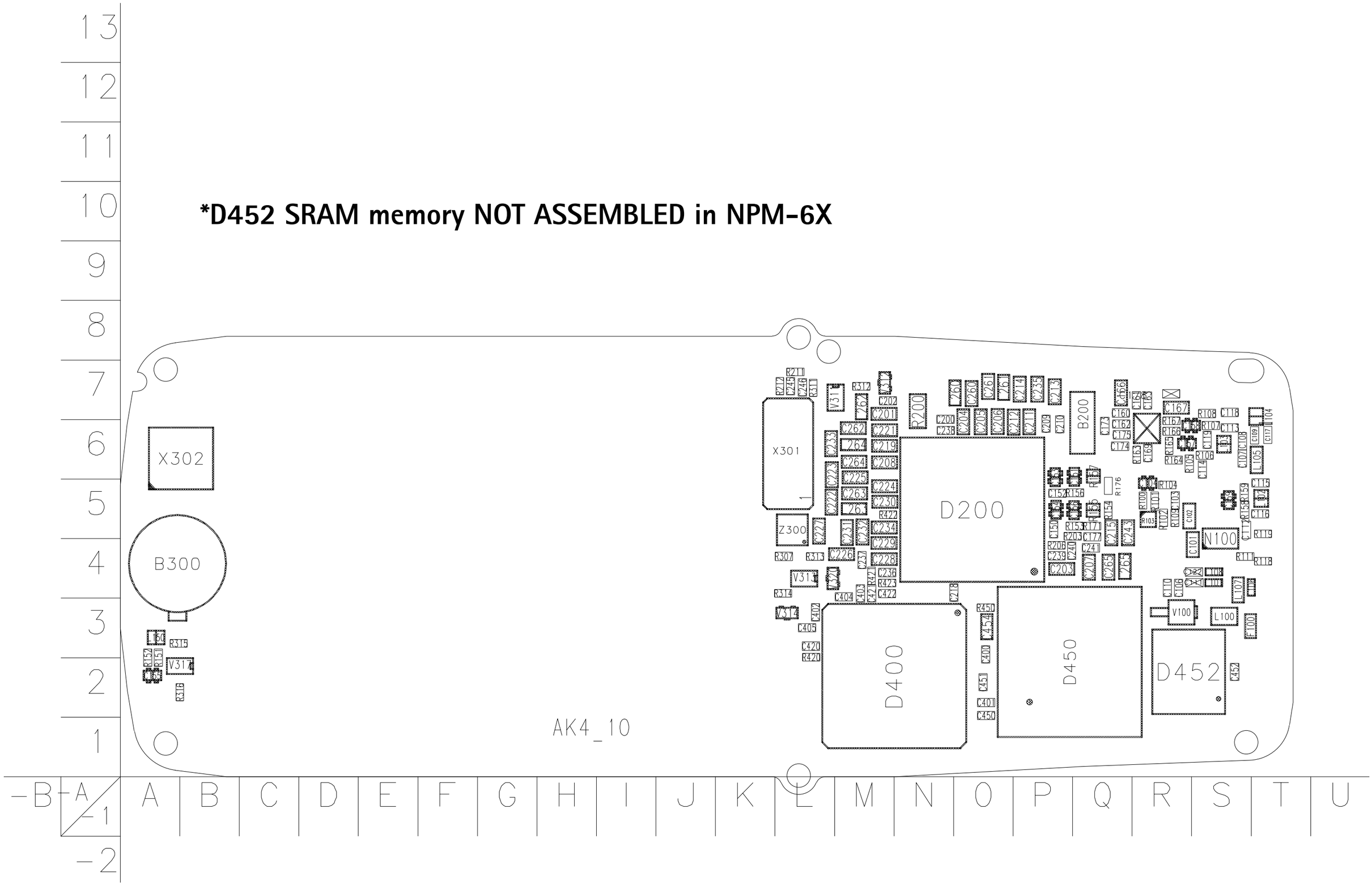
***D452 SRAM memory NOT ASSEMBLED in NPM-6X**



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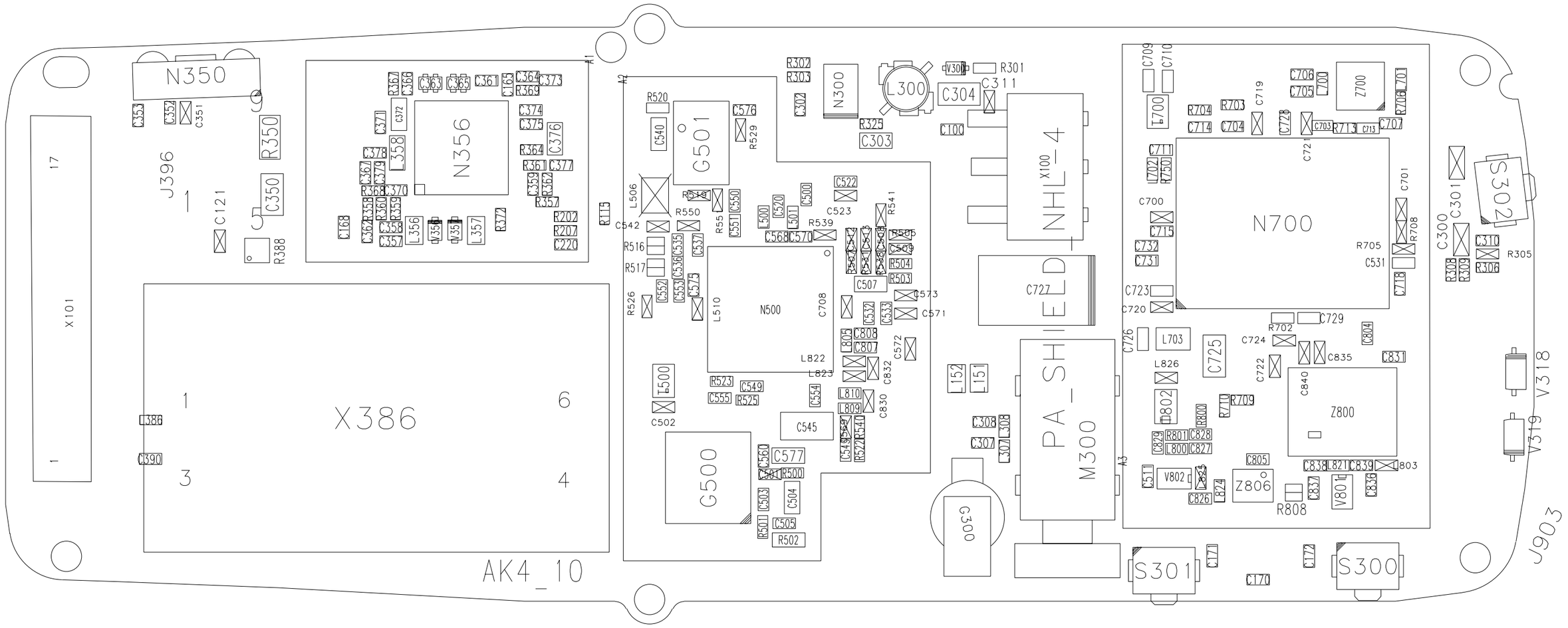
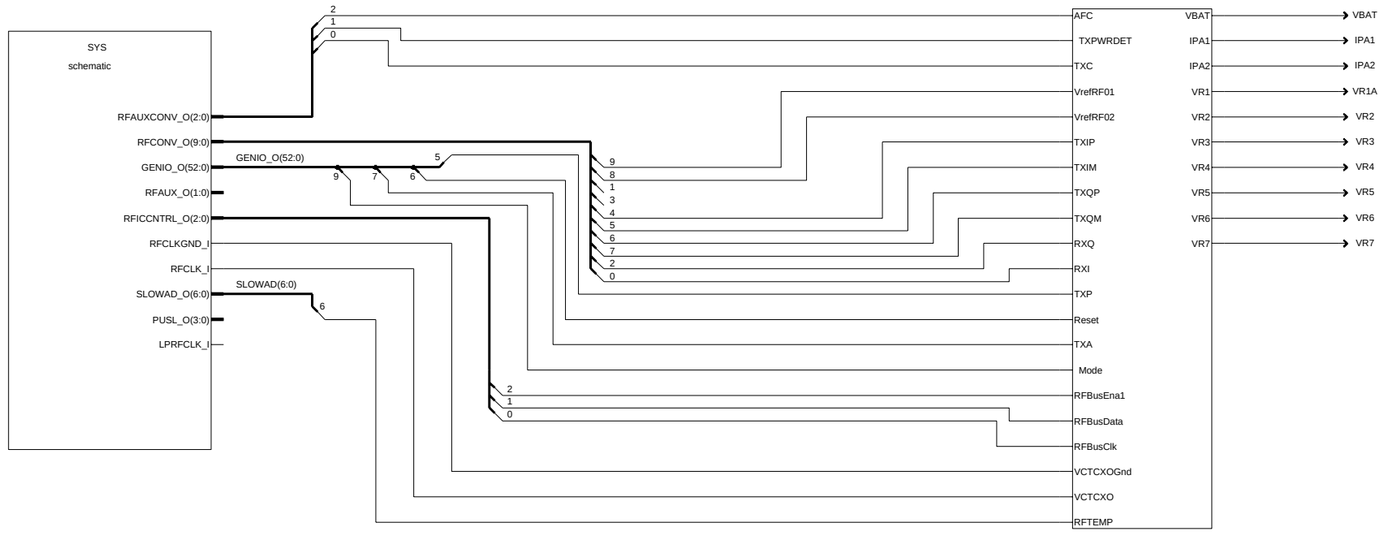
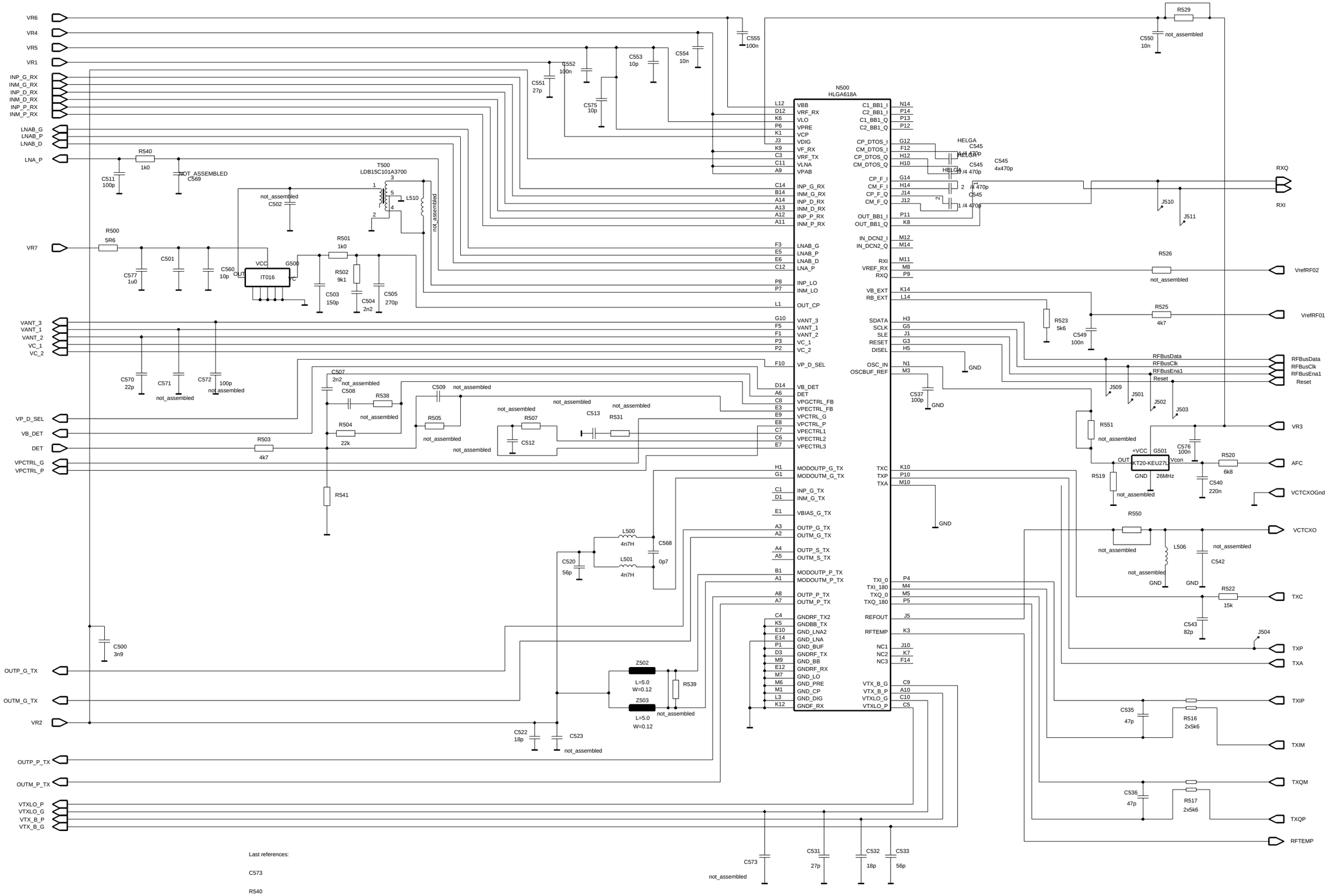


©Nokia Corporation



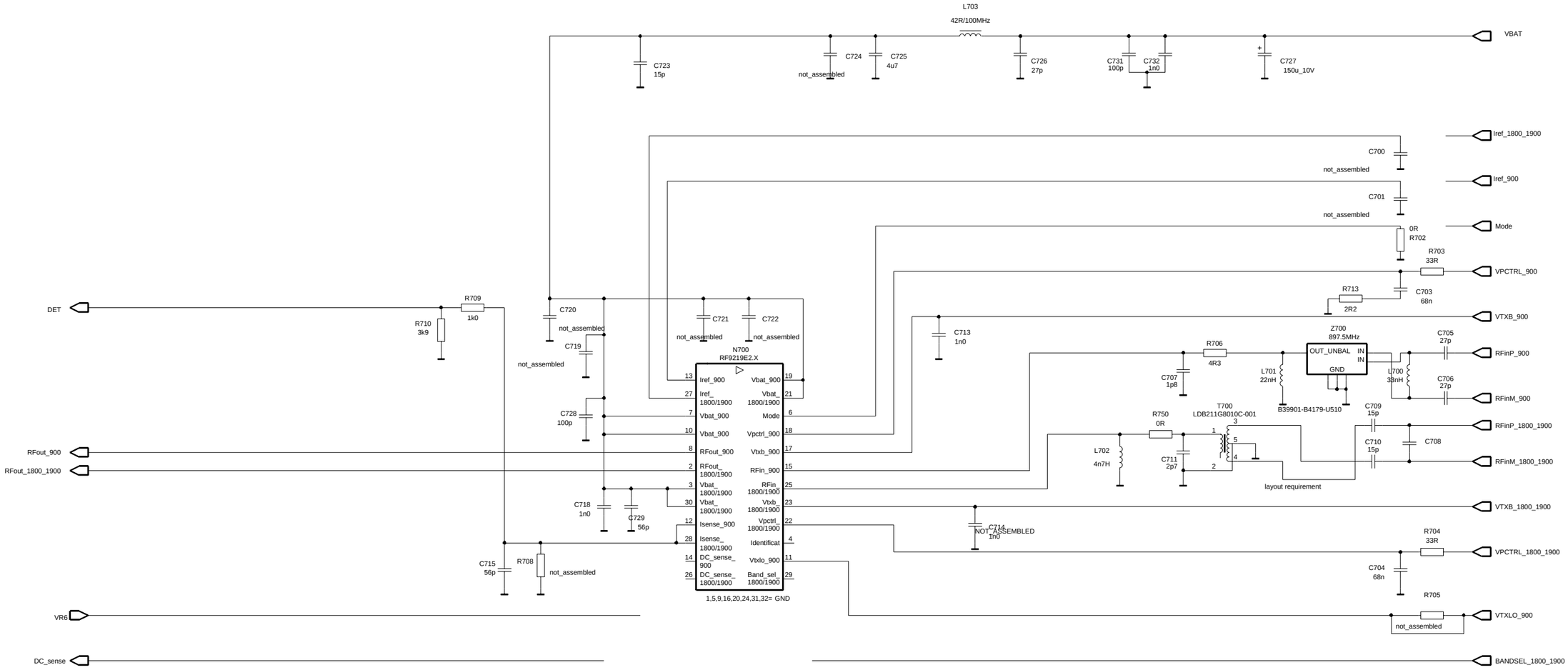
©Nokia Corporation



Last references:

C573

R540



Last references:

C729

R712

L704

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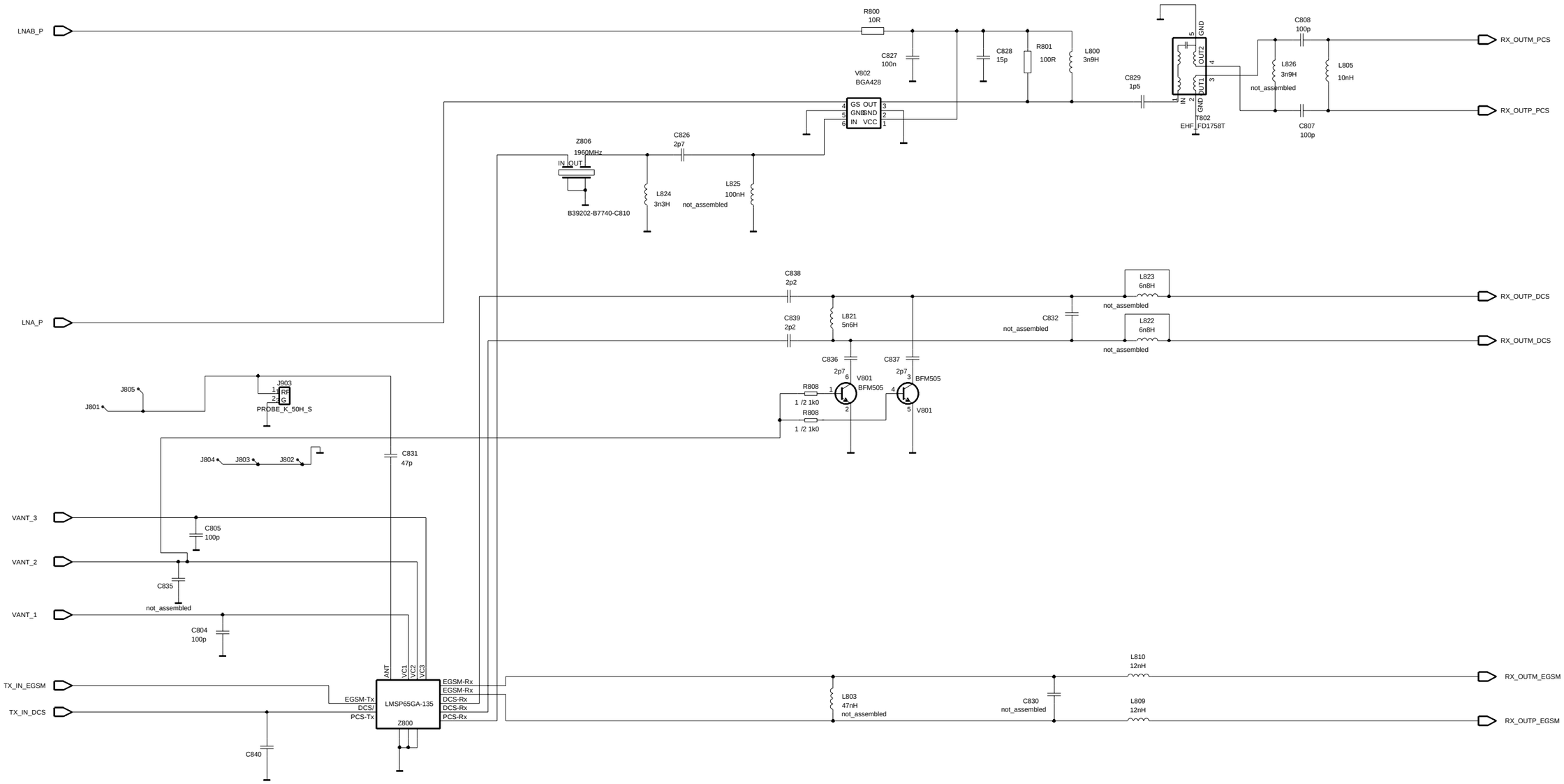


Last references:

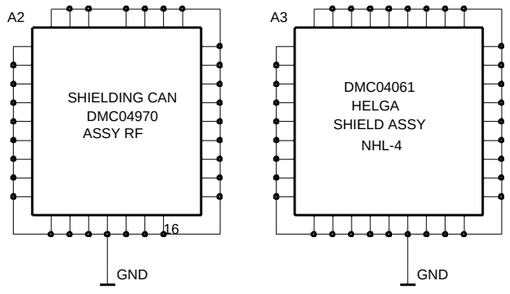
C835

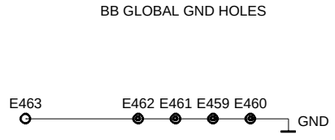
R807

L825

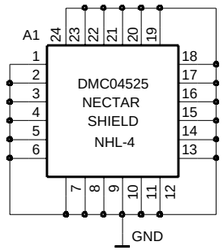
Z806

V800





RF GLOBAL GND HOLES



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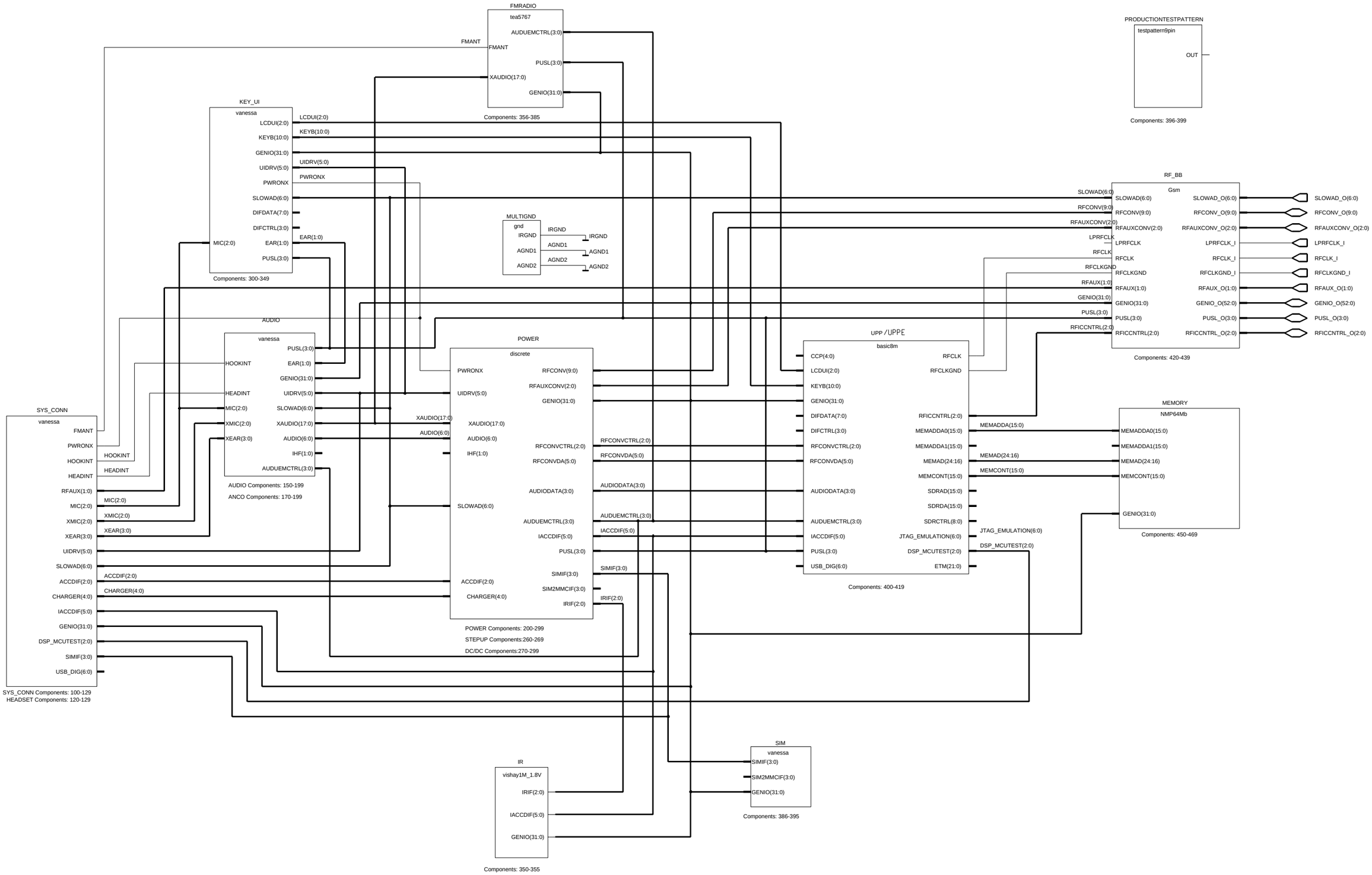
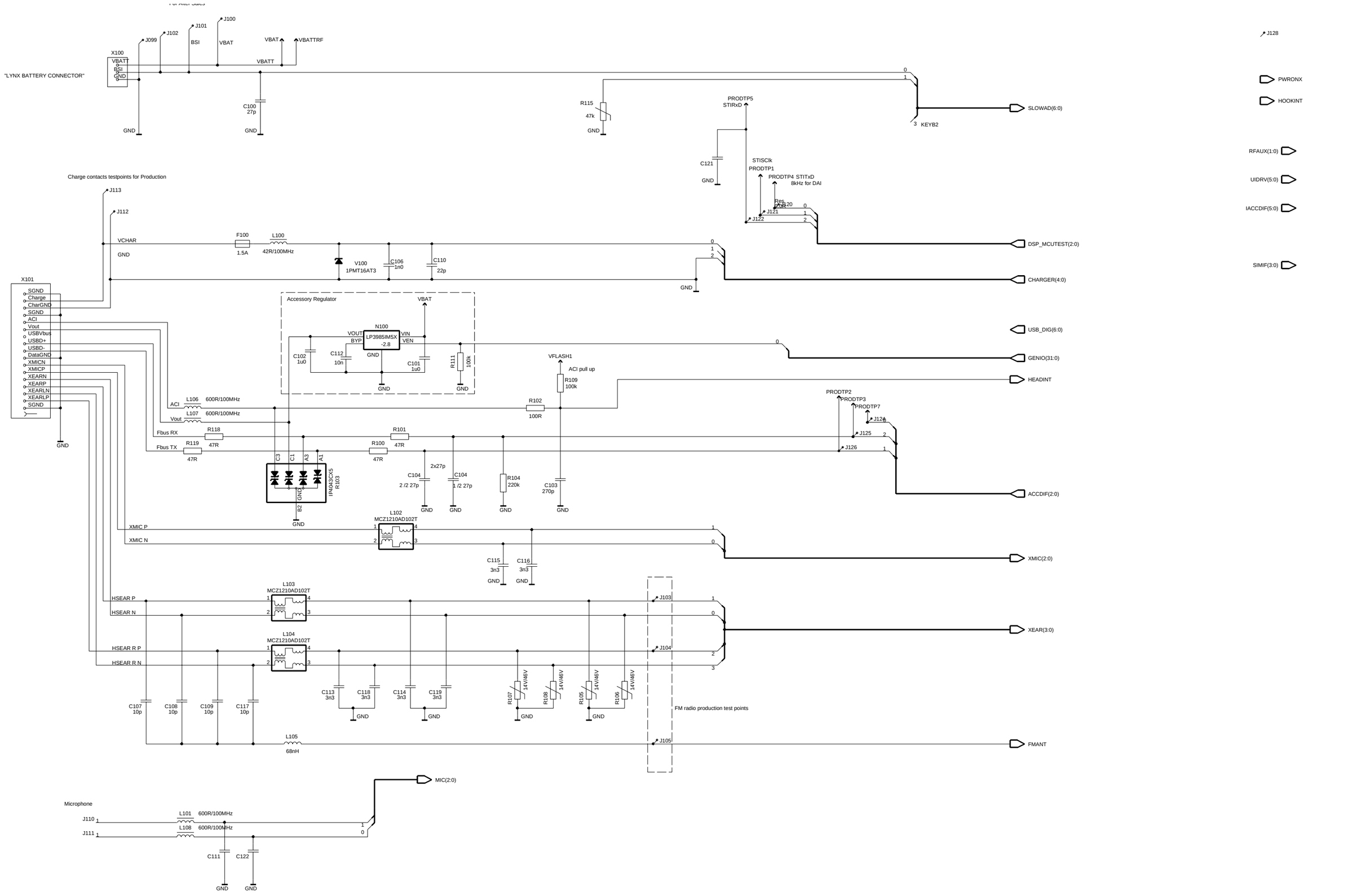

FMRADIO
tea5767
AUDUEMCTRL(3:0)
FMANT
FMANT
PUSL(3:0)
XAUDIO(17:0)
GENIO(31:0)
Components: 356-385
PRODUCTIONTESTPATTERN
testpattern9pin
OUT
Components: 396-399
KEY_UI
vanessa
LCDUI(2:0)
KEYB(10:0)
GENIO(31:0)
UIDRV(5:0)
PWRONX
SLOWAD(6:0)
DIFDATA(7:0)
DIFCTRL(3:0)
MIC(2:0)
EAR(1:0)
PUSL(3:0)
Components: 300-349
MULTIGND
gnd
IRGND
AGND1
AGND2
RF_BB
Gsm
SLOWAD(6:0)
RFCONV(9:0)
RFAUXCONV(2:0)
LPRFCLK
RFCLK
RFCLKGND
RFAUX(1:0)
GENIO(31:0)
PUSL(3:0)
RFICCNTRL(2:0)
SLOWAD_O(6:0)
RFCONV_O(9:0)
RFAUXCONV_O(2:0)
LPRFCLK_I
RFCLK_I
RFCLKGND_I
RFAUX_O(1:0)
GENIO_O(52:0)
PUSL_O(3:0)
RFICCNTRL_O(2:0)
Components: 420-439
AUDIO
vanessa
PUSL(3:0)
HOOKINT
EAR(1:0)
GENIO(31:0)
HEADINT
UIDRV(5:0)
MIC(2:0)
SLOWAD(6:0)
XMIC(2:0)
XAUDIO(17:0)
XEAR(3:0)
AUDIO(6:0)
IHF(1:0)
AUDUEMCTRL(3:0)
AUDIO Components: 150-199
ANCO Components: 170-199
POWER
discrete
PWRONX
RFCONV(9:0)
RFAUXCONV(2:0)
UIDRV(5:0)
GENIO(31:0)
XAUDIO(17:0)
AUDIO(6:0)
IHF(1:0)
RFCONVCTRL(2:0)
RFCONVDA(5:0)
AUDIODATA(3:0)
SLOWAD(6:0)
AUDUEMCTRL(3:0)
IACCDIF(5:0)
PUSL(3:0)
SIMIF(3:0)
ACCDIF(2:0)
SIM2MMCIF(3:0)
CHARGER(4:0)
IRIF(2:0)
POWER Components: 200-299
STEPUP Components:260-269
DC/DC Components:270-299
UPP /UPPE
basic8m
CCP(4:0)
LCDUI(2:0)
KEYB(10:0)
GENIO(31:0)
DIFDATA(7:0)
DIFCTRL(3:0)
RFCONVCTRL(2:0)
RFCONVDA(5:0)
AUDIODATA(3:0)
AUDUEMCTRL(3:0)
IACCDIF(5:0)
PUSL(3:0)
USB_DIG(6:0)
RFCLK
RFCLKGND
RFICCNTRL(2:0)
MEMADDA0(15:0)
MEMADDA1(15:0)
MEMAD(24:16)
MEMCONT(15:0)
SDRAD(15:0)
SDRDA(15:0)
SDRCTRL(8:0)
JTAG_EMULATION(6:0)
DSP_MCUTEST(2:0)
ETM(21:0)
Components: 400-419
MEMORY
NMP64Mb
MEMADDA0(15:0)
MEMADDA1(15:0)
MEMAD(24:16)
MEMCONT(15:0)
GENIO(31:0)
Components: 450-469
SYS_CONN
vanessa
FMANT
PWRONX
HOOKINT
HEADINT
RFAUX(1:0)
MIC(2:0)
XMIC(2:0)
XEAR(3:0)
UIDRV(5:0)
SLOWAD(6:0)
ACCDIF(2:0)
CHARGER(4:0)
IACCDIF(5:0)
GENIO(31:0)
DSP_MCUTEST(2:0)
SIMIF(3:0)
USB_DIG(6:0)
SYS_CONN Components: 100-129
HEADSET Components: 120-129
IR
vishay1M_1.8V
IRIF(2:0)
IACCDIF(5:0)
GENIO(31:0)
Components: 350-355
SIM
vanessa
SIMIF(3:0)
SIM2MMCIF(3:0)
GENIO(31:0)
Components: 386-395



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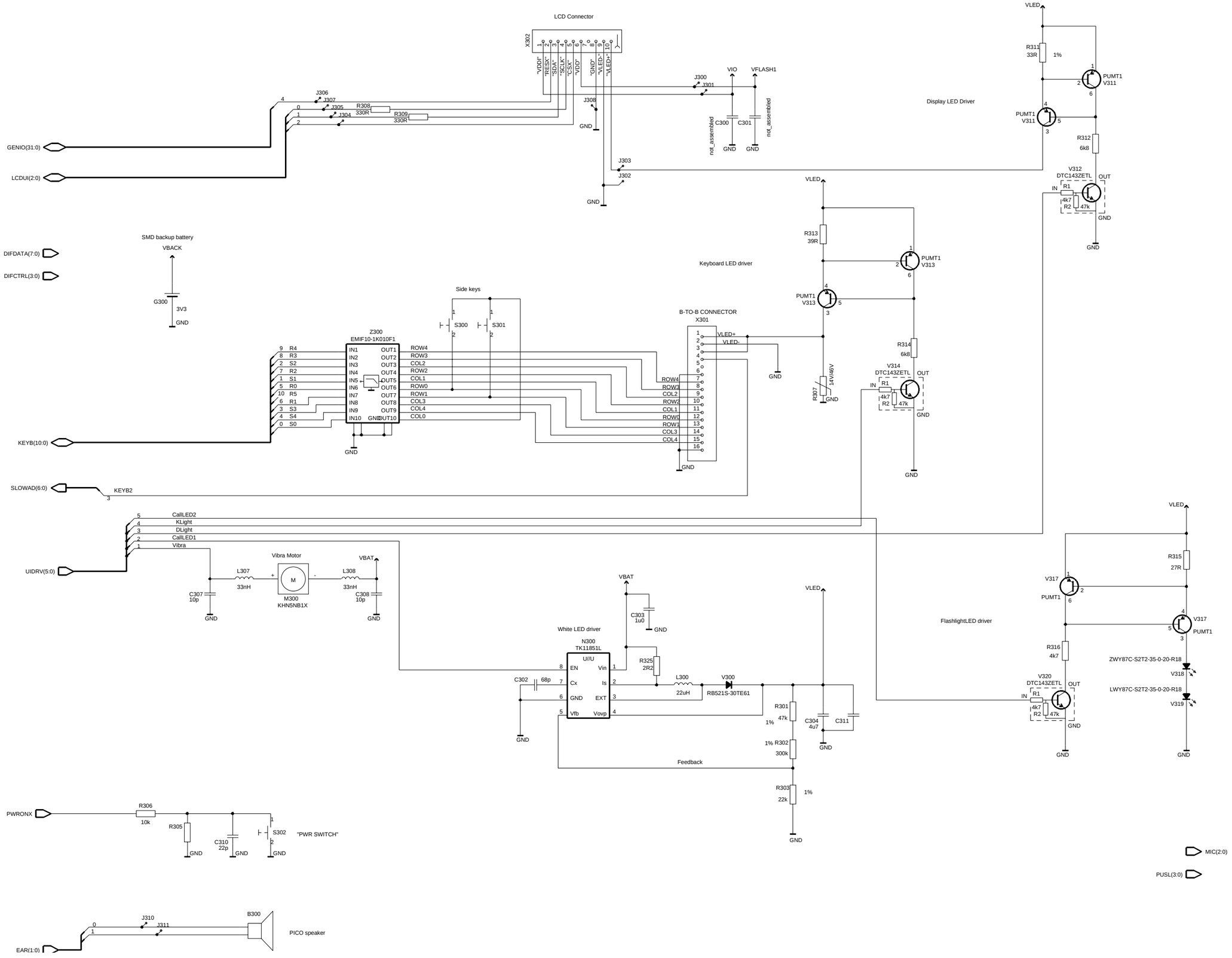

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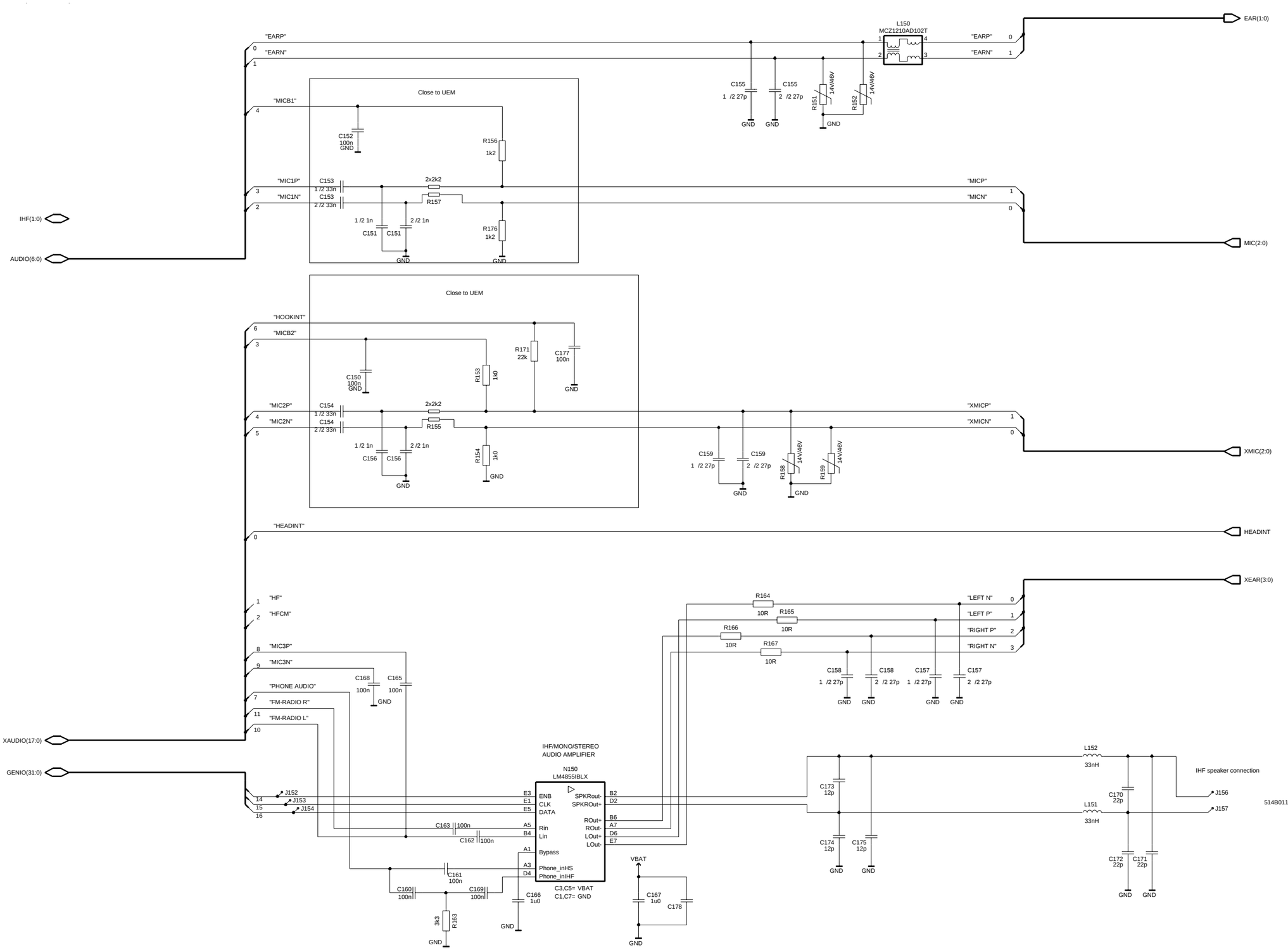

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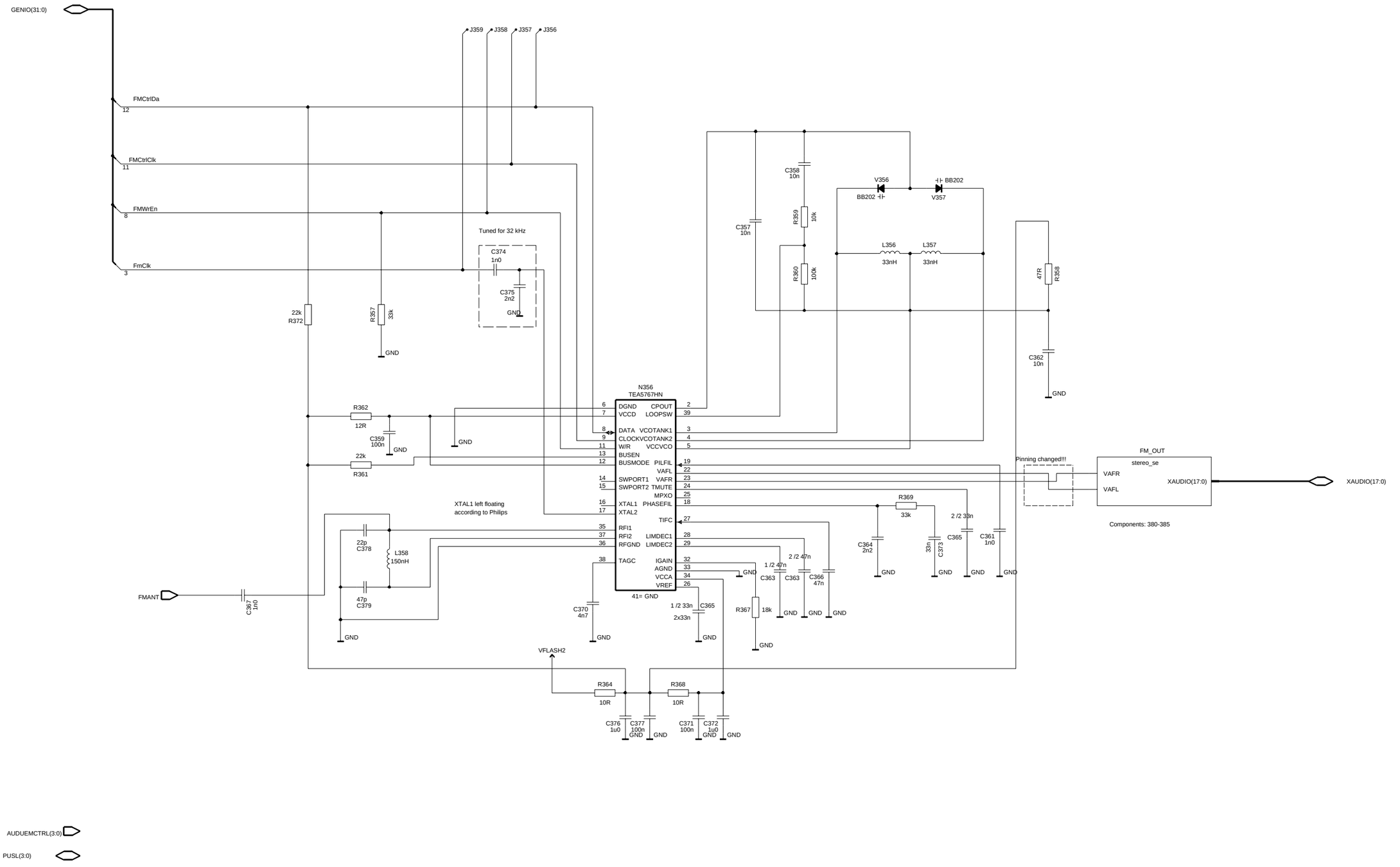
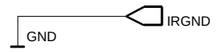
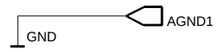
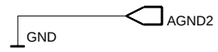

GENIO(31:0)
J359 J358 J357 J356
FMCtrlDa
12
FMCtrlClk
11
FMWrEn
8
FmClk
3
Tuned for 32 kHz
C374 1n0
C375 2n2
GND
22k R372
R357 33k
GND
N356
TEA5767HN
DGND CPOUT 6 2
VCCD LOOPSW 7 39
DATA VCOTANK1 8 3
CLOCK VCOTANK2 9 4
W/R VCCVCO 11 5
BUSEN 13
BUSMODE PILFIL 12 19
VAFL 22
SWPORT1 VAFR 14 23
SWPORT2 TMUTE 15 24
MPXO 25
XTAL1 PHASEFIL 16 18
XTAL2 17
TIFC 27
RFI1 35
RFI2 LIMDEC1 37 28
RFGND LIMDEC2 36 29
TAGC IGAIN 38 32
AGND 33
VCCA 34
VREF 26
41= GND
R362 12R
C359 100n
22k R361
XTAL1 left floating according to Philips
22p C378
L358 150nH
47p C379
FMANT
C367 1n0
C370 4n7
1 /2 33n 2x33n C365
VFLASH2
R364 10R
R368 10R
C376 1u0
C377 100n
C371 100n
C372 1u0
C358 10n
C357 10n
R359 10k
R360 100k
V356 BB202
BB202 V357
L356 33nH
L357 33nH
C362 10n
47R R358
R369 33k
C364 2n2
C373 33n
C365 2 /2 33n
C361 1n0
1 /2 47n
C363
2 /2 47n
C366 47n
R367 18k
Pinning changed!!!
FM_OUT
stereo_se
VAFR
VAFL
XAUDIO(17:0)
XAUDIO(17:0)
Components: 380-385
AUDUEMCTRL(3:0)
PUSL(3:0)







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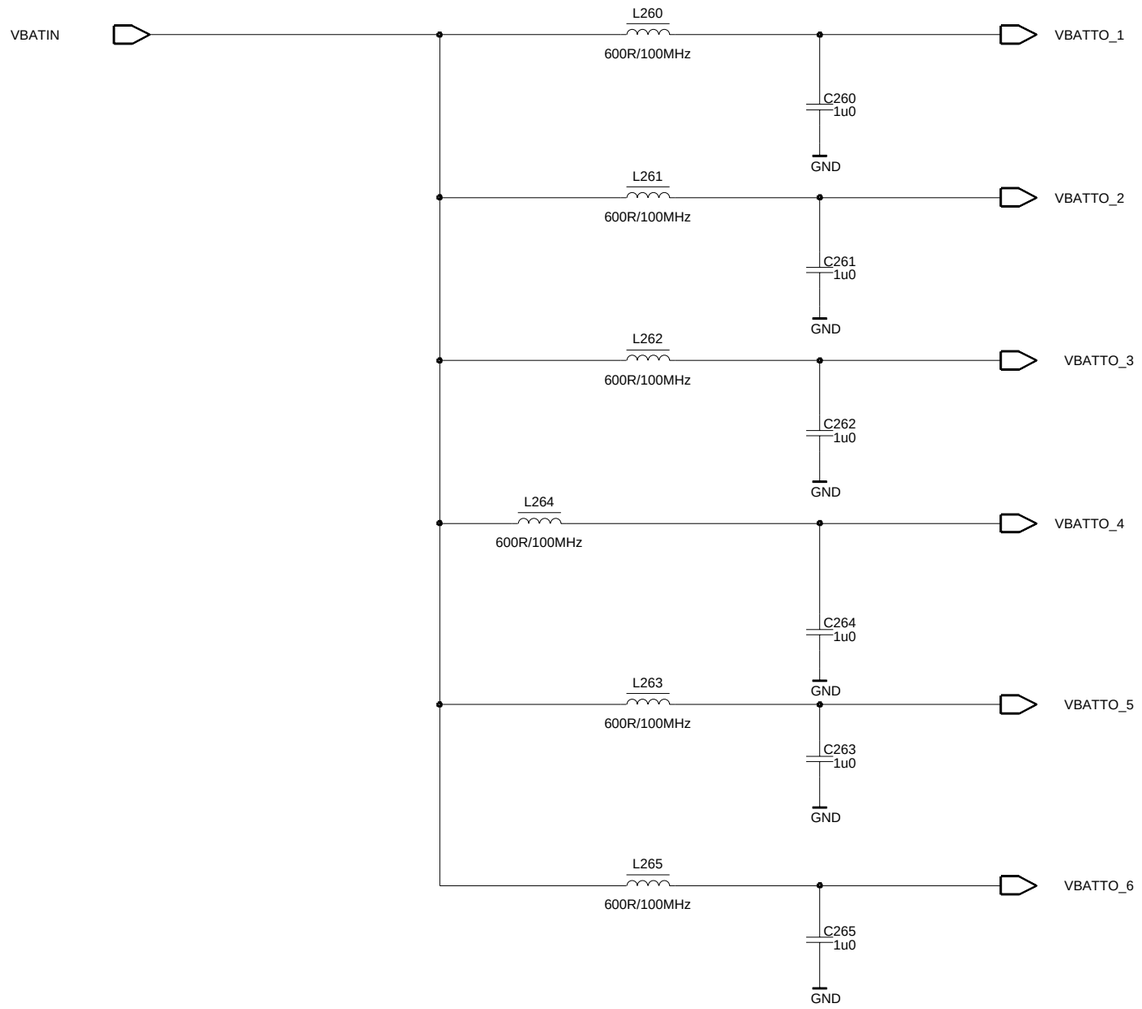

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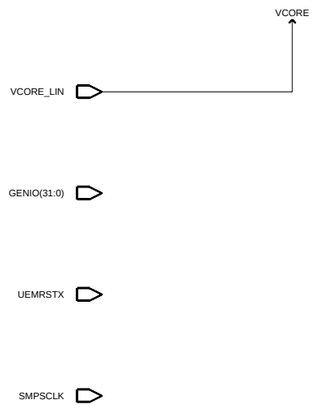

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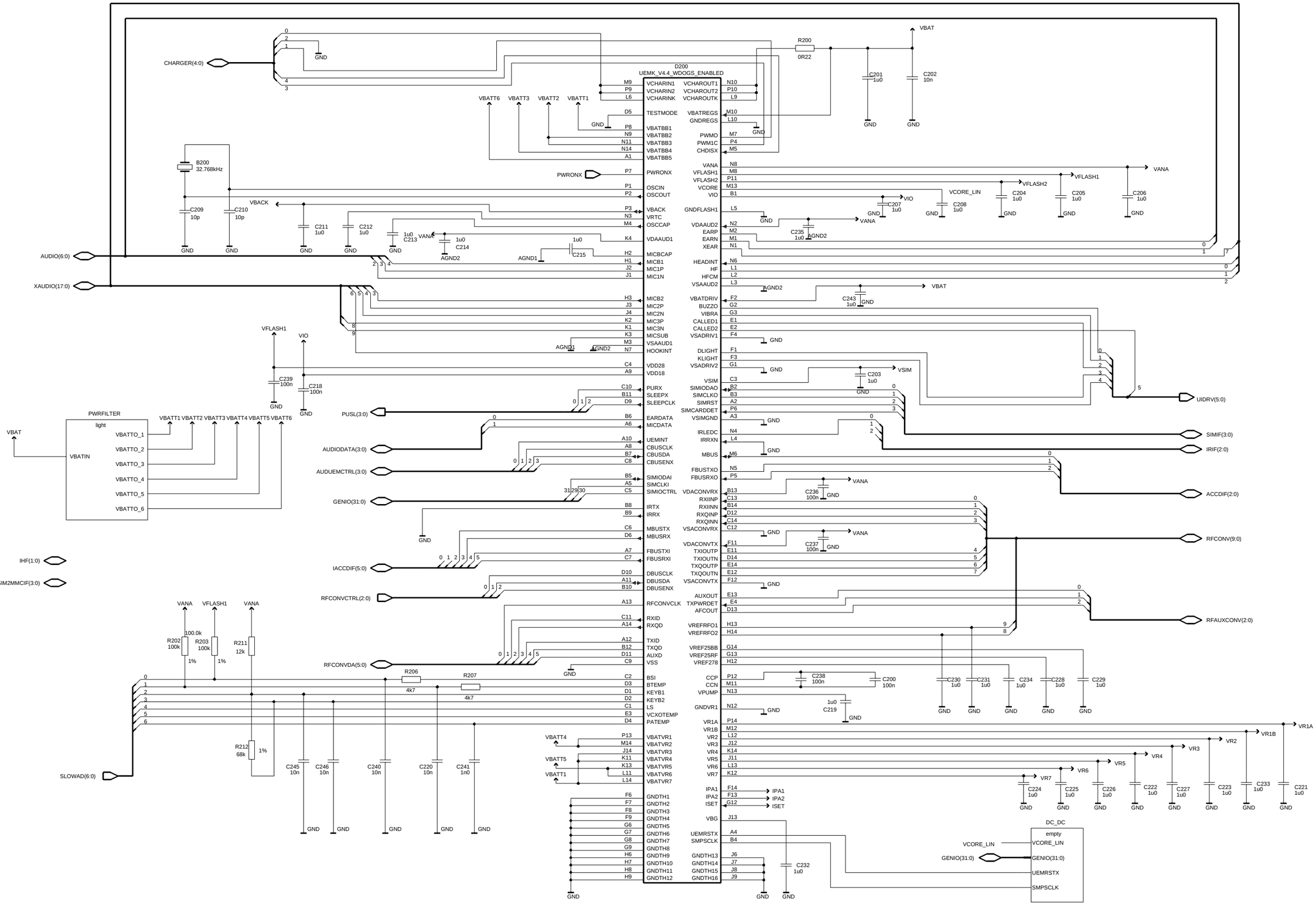

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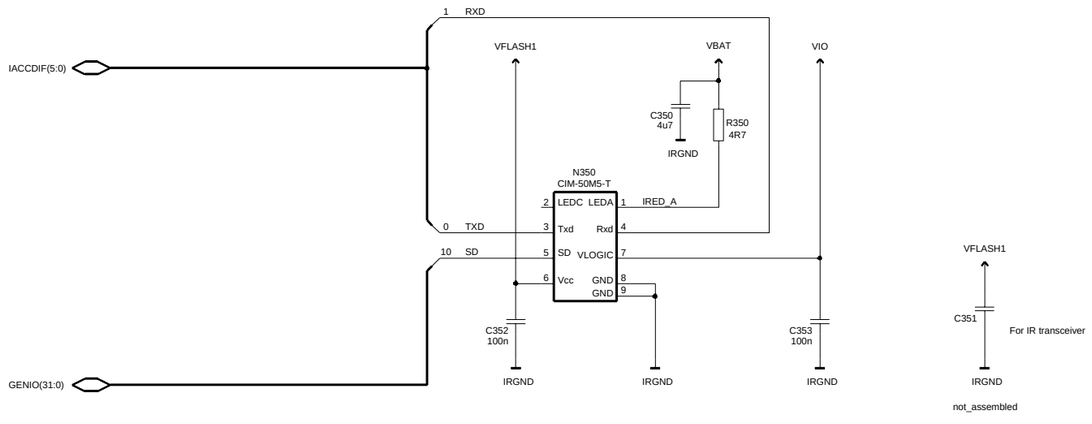

Used referenses

C	350 - 353
N	350
R	350

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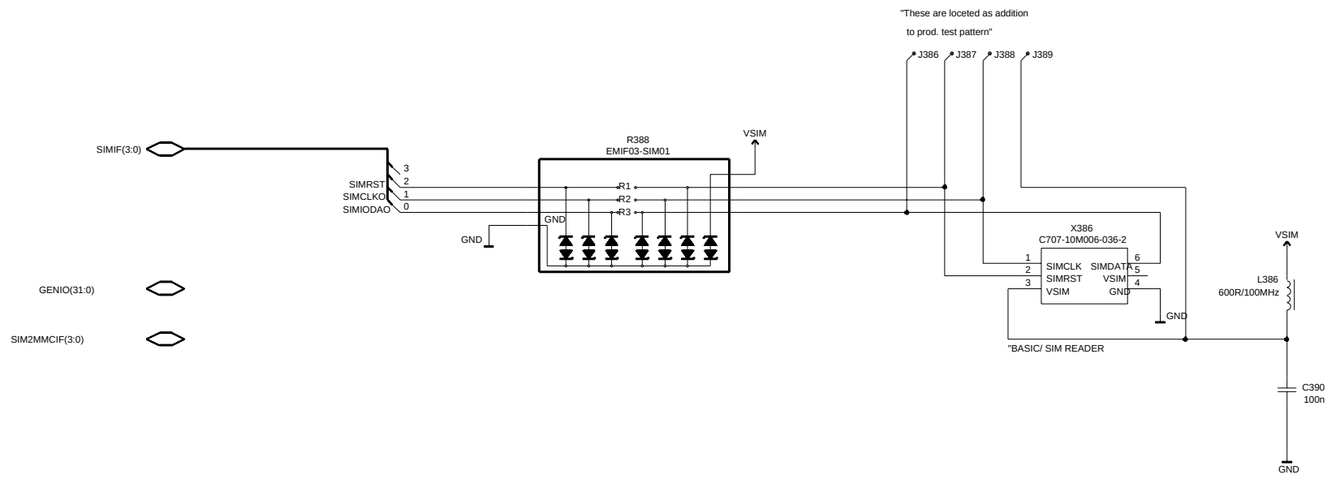

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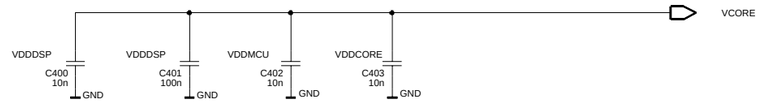

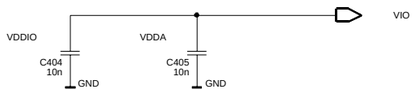

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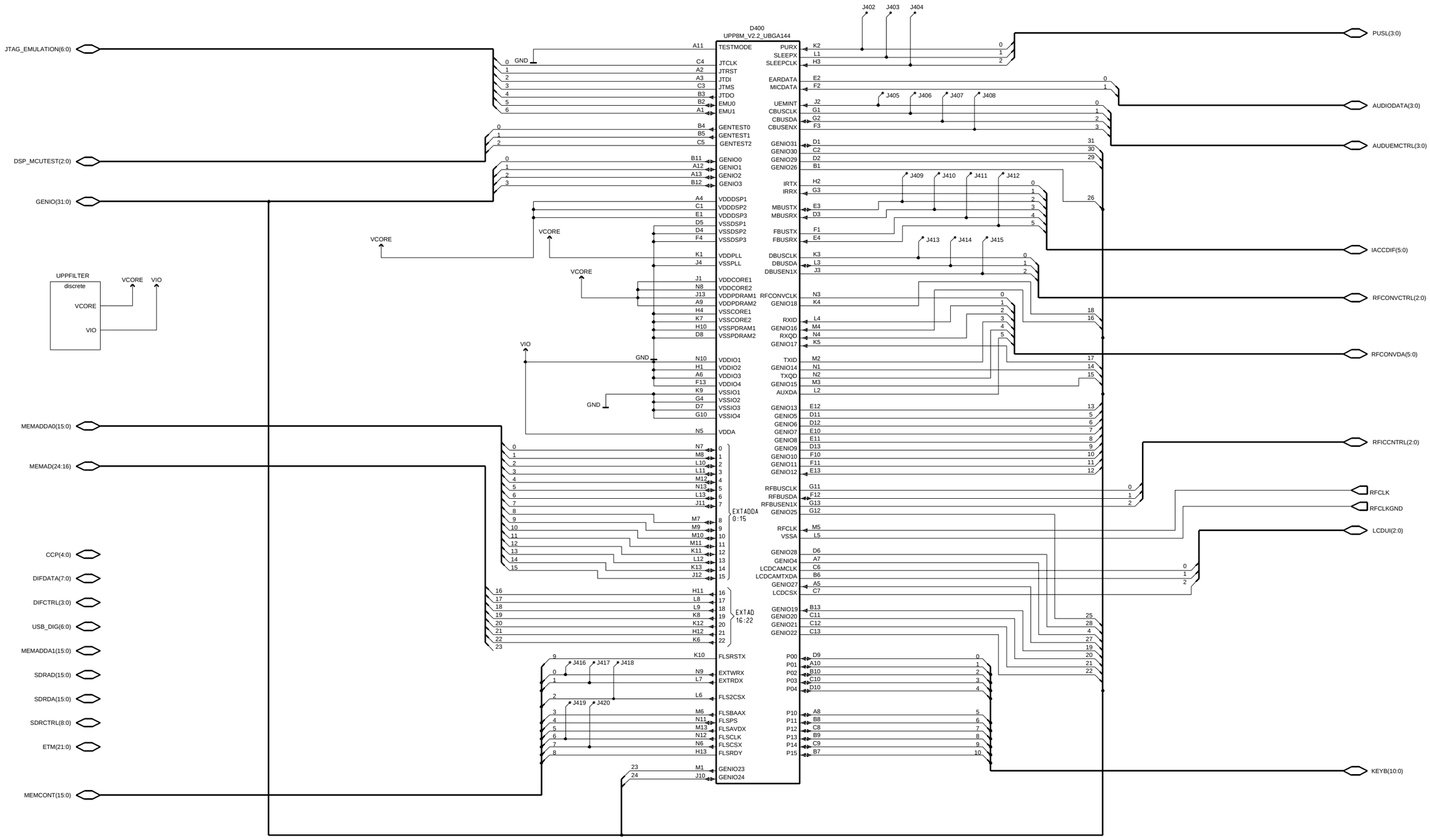

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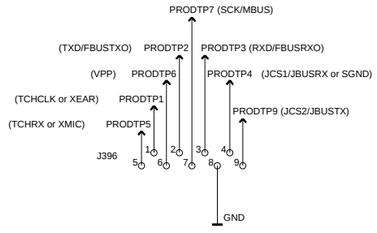

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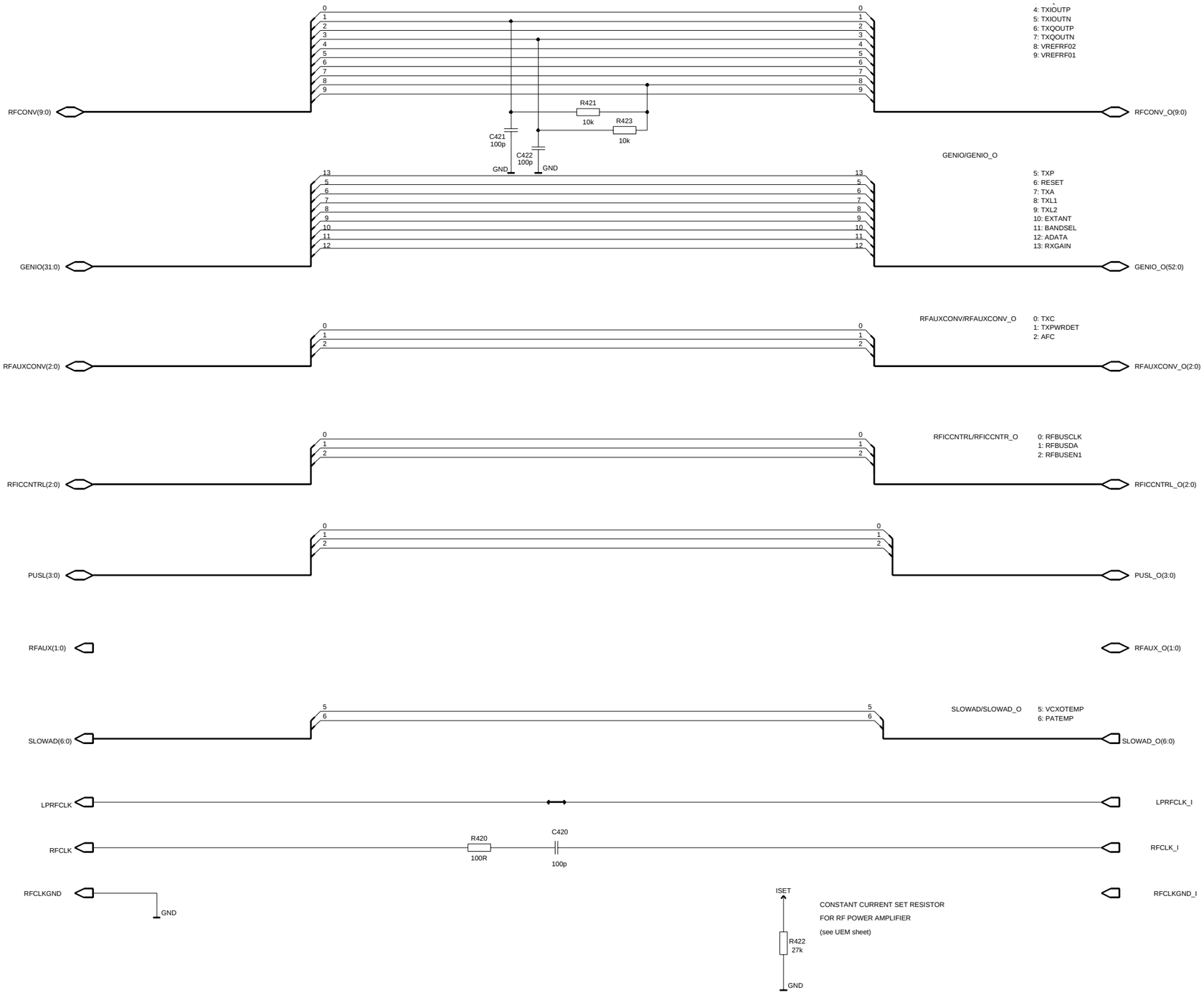

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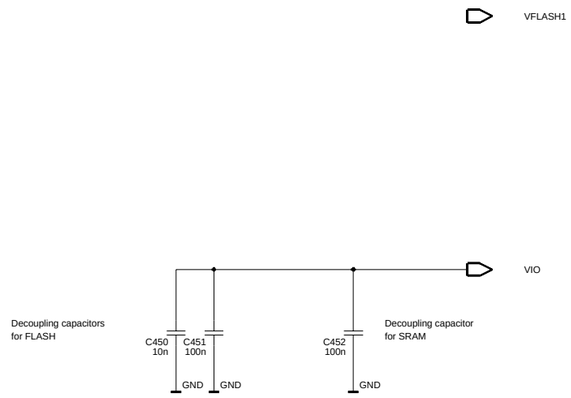

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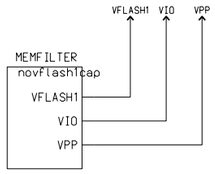
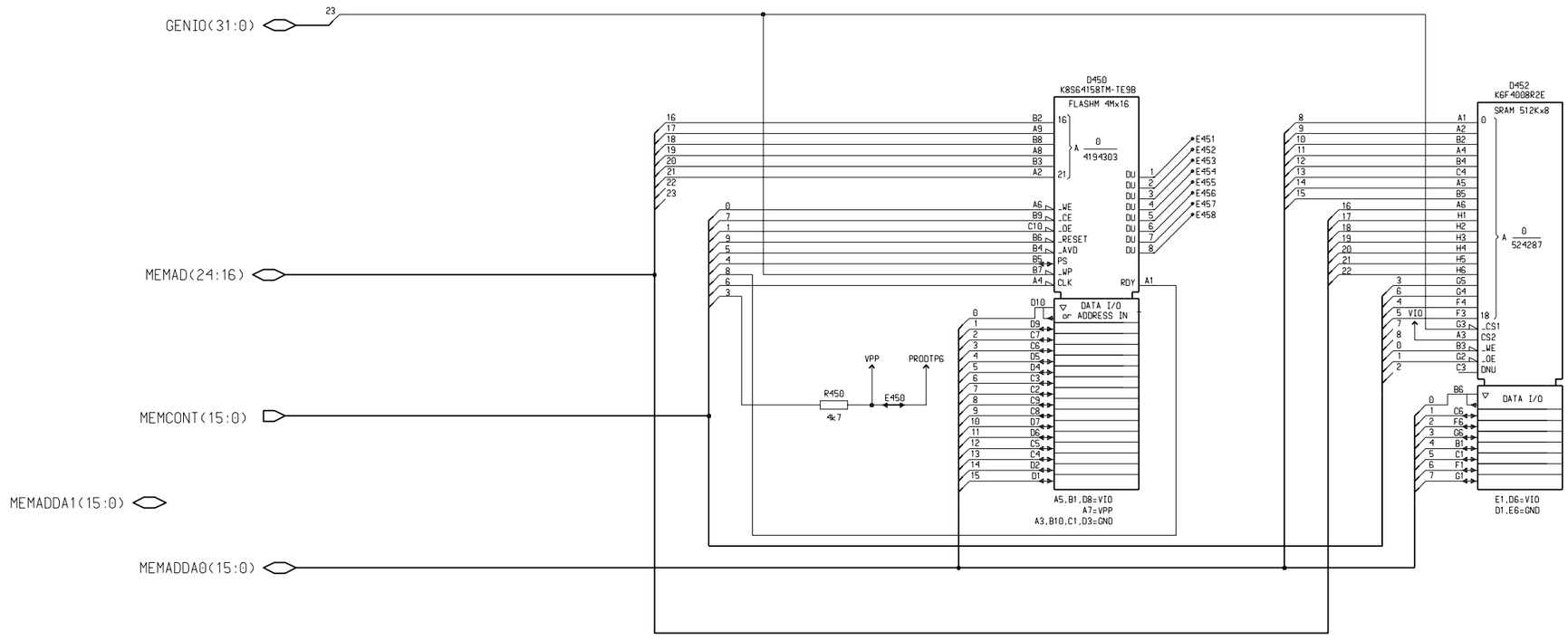


*452 SRAM memory NOT ASSEMBLED in NPM-6X
*450 FLASH memory is COMBO memory in NPM-6X



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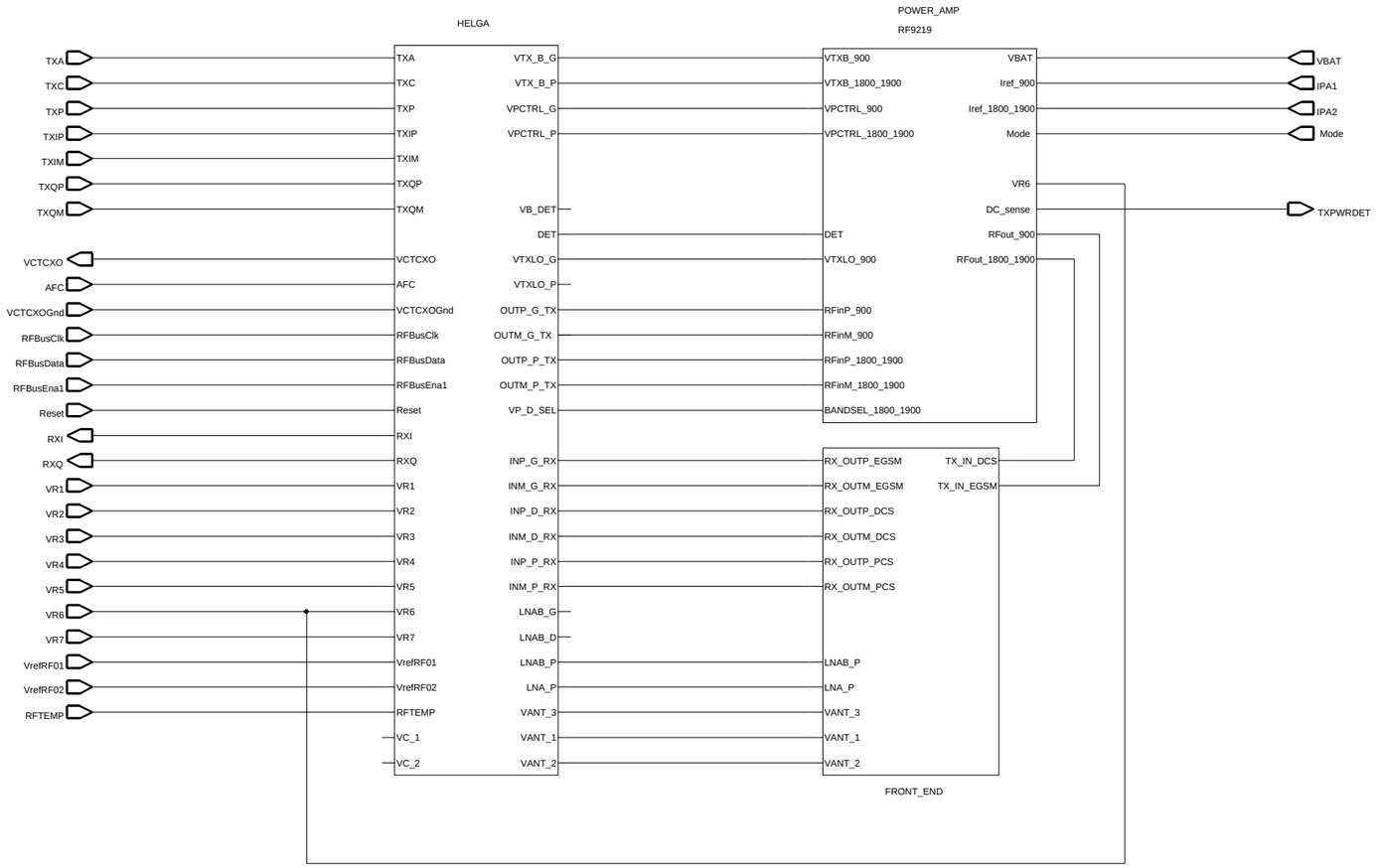
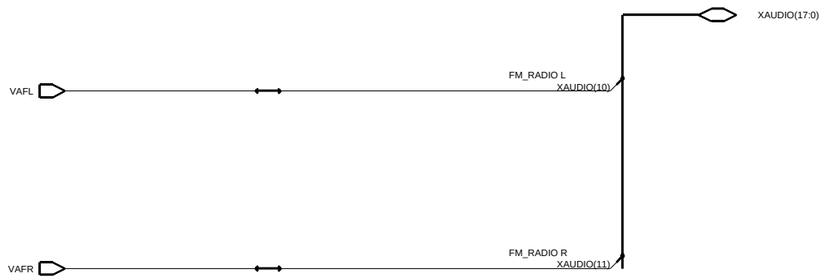

HELGA
POWER_AMP
RF9219
TXA
TXC
TXP
TXIP
TXIM
TXQP
TXQM
VCTCXO
AFC
VCTCXOGnd
RFBusClk
RFBusData
RFBusEna1
Reset
RXI
RXQ
VR1
VR2
VR3
VR4
VR5
VR6
VR7
VrefRF01
VrefRF02
RFTEMP
VC_1
VC_2
VTX_B_G
VTX_B_P
VPCTRL_G
VPCTRL_P
VB_DET
DET
VTXLO_G
VTXLO_P
OUTP_G_TX
OUTM_G_TX
OUTP_P_TX
OUTM_P_TX
VP_D_SEL
INP_G_RX
INM_G_RX
INP_D_RX
INM_D_RX
INP_P_RX
INM_P_RX
LNAB_G
LNAB_D
LNAB_P
LNA_P
VANT_3
VANT_1
VANT_2
VTXB_900
VTXB_1800_1900
VPCTRL_900
VPCTRL_1800_1900
DET
VTXLO_900
RFinP_900
RFinM_900
RFinP_1800_1900
RFinM_1800_1900
BANDSEL_1800_1900
RX_OUTP_EGSM
RX_OUTM_EGSM
RX_OUTP_DCS
RX_OUTM_DCS
RX_OUTP_PCS
RX_OUTM_PCS
LNAB_P
LNA_P
VANT_3
VANT_1
VANT_2
VBAT
Iref_900
Iref_1800_1900
Mode
VR6
DC_sense
RFout_900
RFout_1800_1900
TX_IN_DCS
TX_IN_EGSM
VBAT
IPA1
IPA2
Mode
TXPWRDET
FRONT_END



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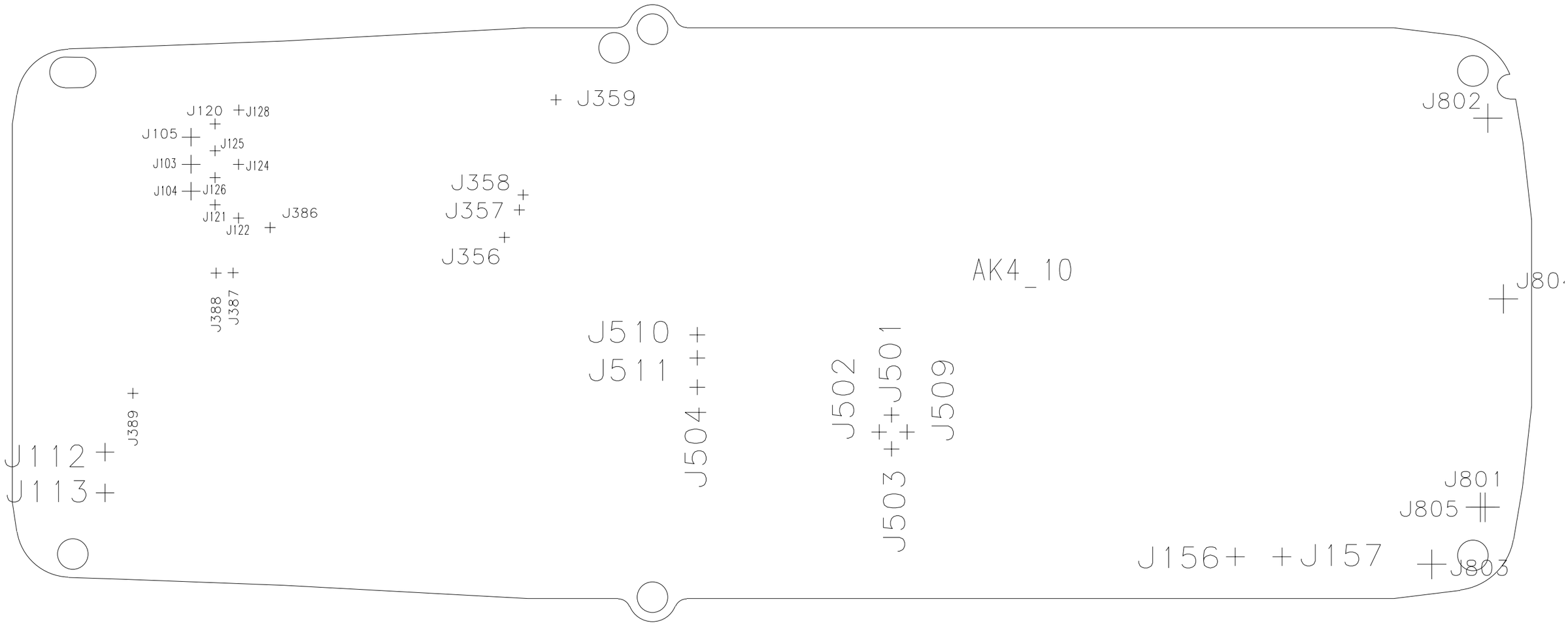

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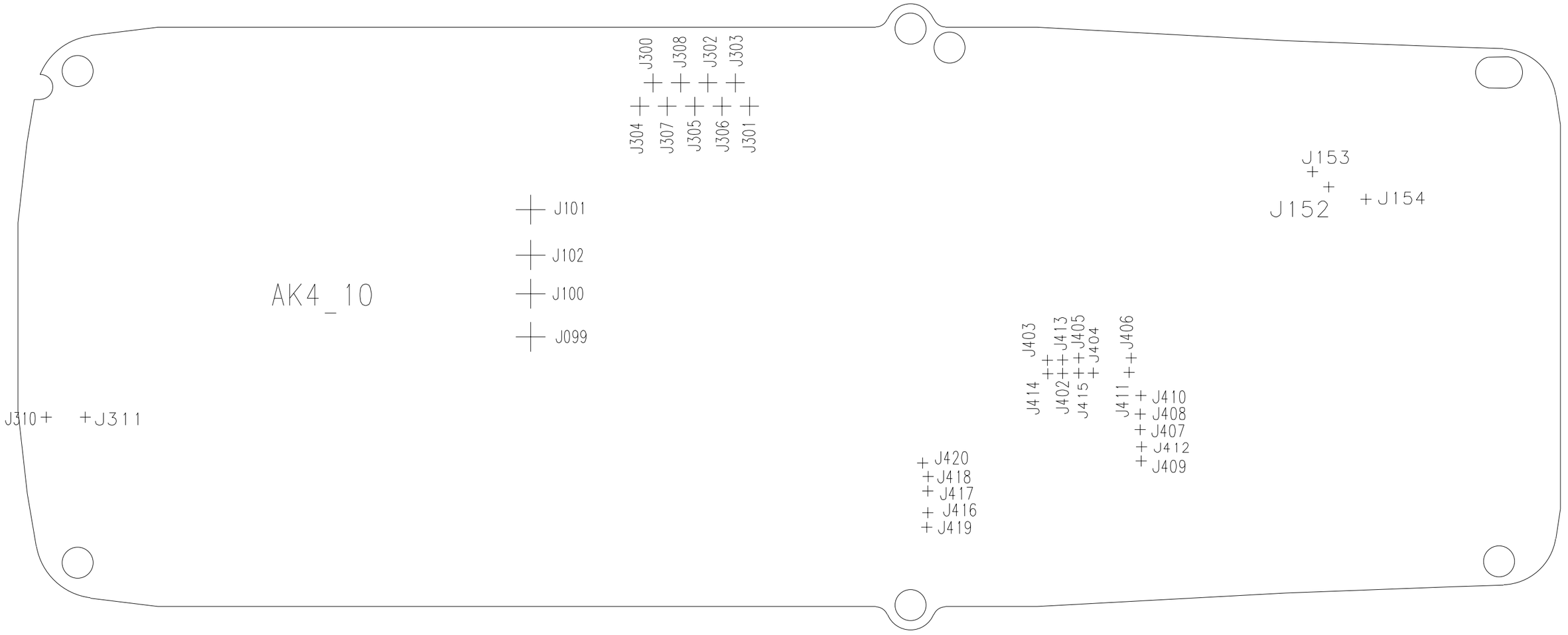

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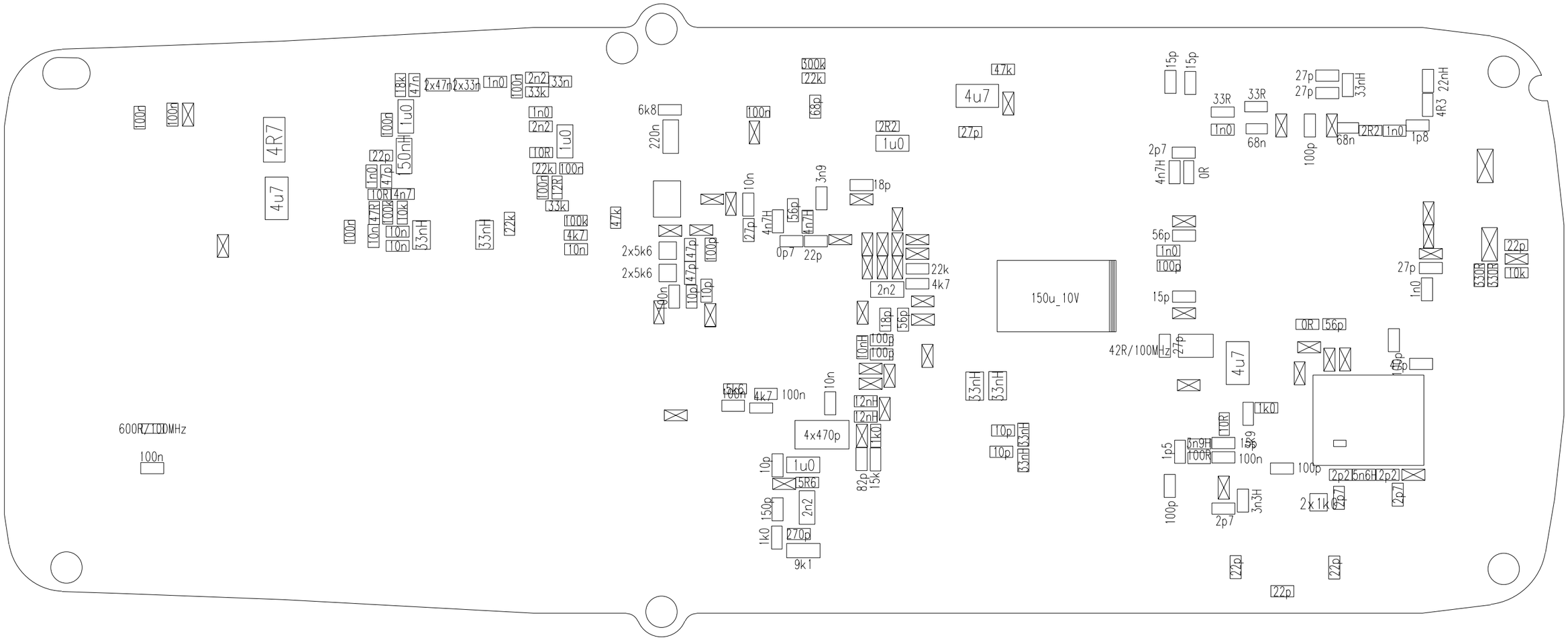

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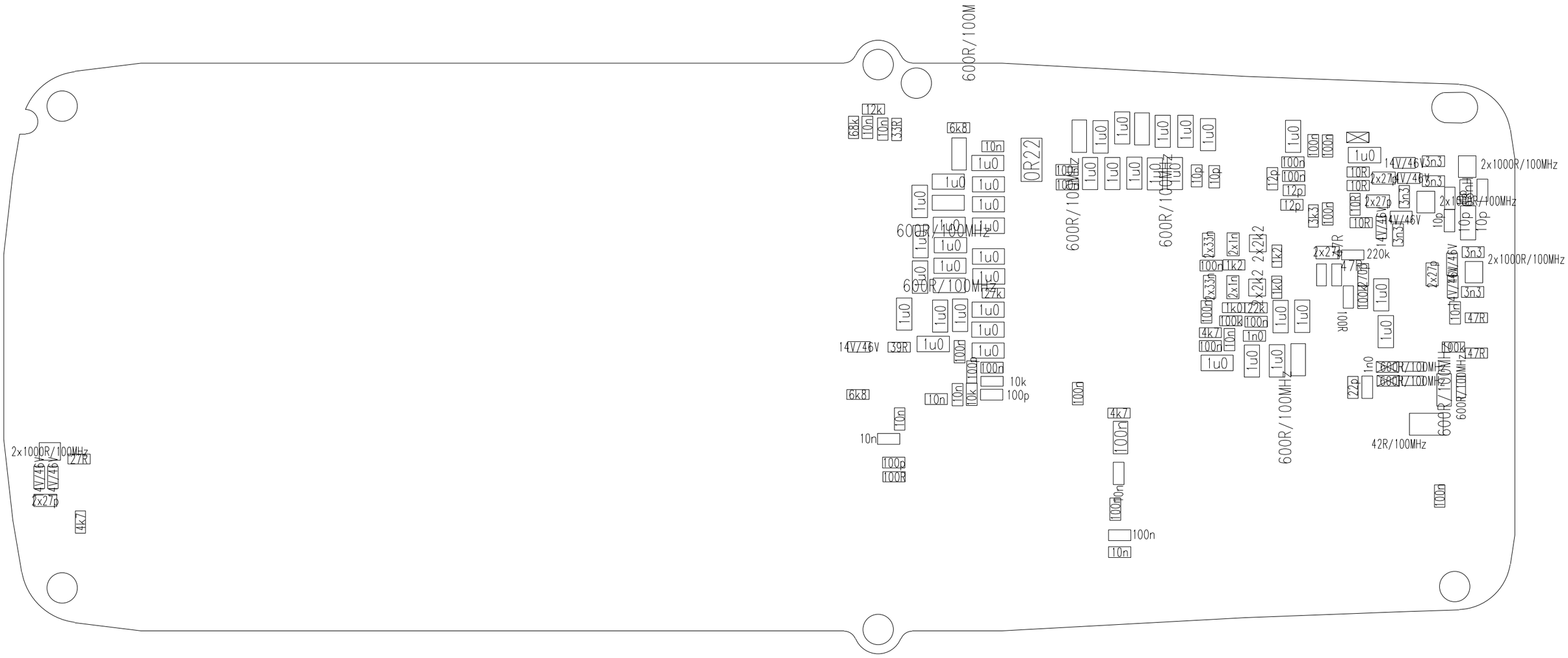

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Signal	Test point	Function	Characteristics	Note
FBUSTXO	PRODTP2	Flash programming data and phone control	2.78V digital signal	From phone to FPS-8/PC
FBUSRXO	PRODTP3	Flash programming data and phone control	2.78V digital signal	From FPS-8/PC to phone
VPP	PRODTP6	Flash programming voltage	1.8V internal voltage 12V external voltage	
MBUS	PRODTP7	Flash programming clock and phone control	2.78V digital signal 6.5 MHz max.	Bi-directional phone control
BSI	J101,J102 or battery connector	Battery size indicator Local mode indicator SIM removal indicator Flash programming start signal	1V in normal mode 0V in local mode If BSI line rises > 2.1V 2.78V BSI pulse	To UEM A/D converter
BTEMP	R157/R203	Battery temp. indicator Test mode indicator	About 0.8V at 25°C 0V in test mode	
SIMDATA	J386	SIM data	Digital signal 1.8 / 3V	From / to UEM / SIM card
SIMRST	J387	SIM reset	Digital signal 1.8 / 3V	From UEM to SIM card
SIMCLK	J388	SIM clock	3.25MHz digital clock signal 1.8 / 3V	From UEM to SIM card
VSIM	J389	Power supply for SIM card	1.8V or 3V	Depends on the SIM card
PURX	J402	Power up reset	1.8V digital signal	From UEM to UPP
SLEEPX	J403	Sleep mode control signal	1.8V when not in sleep 0V when in sleep mode	
SLEEPCLK	J404	Sleep mode timing clock	32.768kHz digital clock 1.8V	
UEMINT	J405	Interrupt request for UPP	1.8V digital signal	From UEM to UPP
CBUSCLK	J406	Serial control bus clock	1MHz digital clock signal 1.8V	From UPP (MCU) to UEM Controlled by MCU
CBUSDA	J407	Serial control bus data input/output	1.8V digital signal	Between UPP (MCU) and UEM Controlled by MCU
CBUSENX	J408	CBUS enable signal	1.8V digital signal	From UPP (MCU) to UEM Controlled by MCU

Table 2: TEST POINTS

Signal	Test point	Function	Characteristics	Note
RFBUSDA	J509	HELGA control serial data	1.8V digital signal	From UPP to HELGA
RFBUSCLK	J501	HELGA control clock	26MHz digital clock signal 1.8V	From UPP to HELGA
RFBUSEN1	J502	HELGA chip select and latch enable	1.8V digital signal	From UPP to HELGA
RFBUS reset (GENIO6)	J503	Helga chip reset	1.8V digital signal	From UPP to HELGA
RFCLK	R420	System clock for baseband	26MHz analog clock signal > 300mVpp	From HAGAR to UPP
RESX	J306	LCD reset	1.8V digital signal	From UPP to LCD driver
CSX	J304	LCD chip select	1.8V digital signal	From UPP to LCD driver
SDA	J305	LCD serial data	1.8V digital signal	From UPP to LCD driver
SCLK	J307	LCD clock	3.25MHz digital clock signal 1.8V	From UPP to LCD driver
VDDI	J301	Supply voltage (driver)	2.78V	=VFLASH1
VDD	J300	Supply voltage (logic)	1.8V	=VIO
VLED+	J303	Supply voltage for display LEDs	Min. 7.0V,typ 7.3V max. 8.4V	
VLED-	J302	Return current for VLED+		GND
Display GND	J308	Ground		
MBUSTX	J409	MBUS from UPP to UEM	1.8V digital signal	
MBUSRX	J410	MBUS from UEM to UPP	1.8V digital signal	
FBUSTX	J411	FBUS from UPP to UEM	1.8V digital signal	
FBUSRX	J412	FBUS from UEM to UPP	1.8V digital signal	
DBUSCLK	J413	DBUS clock	13MHz digital clock signal 1.8V	From UPP (DSP) to UEM Generated by UPP
DBUSDA	J414	DBUS data input/output	1.8V digital signal	Between UEM and UPP (DSP)
DBUSEN1X	J415	DBUS selection and enable	1.8V digital signal	From UPP (DSP) to UEM
EXTWRX	J416	Flash memory write enable	1.8V digital signal	
EXTRDX	J417	Flash memory read enable	1.8V digital signal	
FLSCLK	J419	Flash memory clock	35MHz digital clock signal 1.8V	In burst mode
FLSCSX	J420	Flash memory chip select	1.8V digital signal	

Signal	Test point	Function	Characteristics	Note
Charge	J113	Charging voltage	Max. 16V	For production
ChargeGND	J112	Charging GND	Ground	For production
EARP	J310	Earpiece audio, diff.		From UEM
EARN	J311	Earpiece audio, diff.		From UEM
FMCtrlDa	J356	FM radio control data	1.8V digital signal	From UPP to TEA5767 GENIO(12)
FMCtrlClk	J357	FM radio control clock	1.8V digital signal	From UPP to TEA5767 GENIO(11)
FMWrEn	J358	FM radio enable	1.8V digital signal	
FMClk	J359	FM radio system clock	1.8V digital signal, 32kHz	From UPP to TEA5767 GENIO(3)

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